Original Article

Analysis of Coplanar Capacitive Coupled Wideband Microstrip Antennas

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Abstract - In this paper, input impedance analysis of rectangular, modified rectangular (one side fractal/staircase shaped), triangular, and semi-elliptical capacitive coupled coplanar microstrip antennas have been presented. The proposed models presented here are for capacitive coupled suspended geometries. These models are developed based on the simple circuit theory approaches, which also include the coaxial feed and capacitively coupled feed strip placed outside the radiator patch. In all cases presented, the calculated results fairly agree with the experimental and simulated data. The proposed models help in finding the input impedance of the capacitively coupled antennas through computer-aided (CAD) models.

Keywords — *Capacitive coupling, Input impedance, Semi-Elliptical MSA, ETMSA*

I. INTRODUCTION

Microstrip antennas are becoming more and more popular for 5G and recent wireless applications [1-3]. However, finding input impedance is the main concern for proper tuning of the microstrip antenna. Therefore, the input impedance of microstrip antennas is a vital component in finding the exact coupling between antenna terminals and the lines carrying signals to be transmitted and/or received. Several works on finding the input impedances of microstrip antennas have been reported in the literature [4-16]. Some of these works are transmission line model [4], cavity-based approach [5], method of moment solutions [9], and circuit modeling approaches [6, 10-15], etc. However, some of these techniques involve solving complicated integrals and/or require huge memory/time of the computer. However, the works reported by researchers on circuit models [6, 10-20] for the analysis of antenna input characteristics are not only simple to understand and nearly accurate but also provide physical insight into the antenna geometries. In another work, the input impedance analysis of capacitive coupled coaxial probe fed coplanar UWB MSA was reported by [16], which is based on the method of moment's (full wave) analysis. But it is noteworthy that the reported technique requires rigorous calculations for the basic radiator patch. The input impedance calculation using the equivalent circuit approach for the rectangular geometry is reported in [18, 21].

However, these cover only basic rectangular microstrip geometry. In another work, authors of [23] have attempted to model the slit-loaded truncated microstrip antenna based on [21] and [22].

In this paper, the input impedance analysis of capacitively coupled with coaxial-probe fed coplanar wideband microstrip antennas is presented. Almost all models available in the literature assume that the coaxial feed is directly connected to the radiator element. However, the presented model can be used for finding input impedance based on the circuit theory approach, which also includes the coaxial feed placed outside the patch element into the whole model. Further in the literature, input impedance analysis with this type of feed are available for only rectangular geometries. But this paper covers other complicated geometries (modified RMSA, ETMSA, and semi-ellitical MSAs) for input impedance analysis. The developed techniques presented here are easy to evaluate and are best suited for realization with computer-aided design models.

Section II-A includes the analysis of the input characteristics (simulated, measured, and calculated from our model) of basic rectangular geometry [22]. The model developed here is generalized by extending it to find the input impedance of modified rectangular geometry and is covered in subsection II B. Further, input impedance analysis of other shapes of the capacitively coupled antenna (Triangular and Semi-Ellipse) is presented in Section III. Finally, the conclusions of the work presented in this paper are covered in Section IV.

II. BASIC ANTENNA AND ITS IMPEDANCE ANALYSIS

Analysis of the basic rectangular patch antenna is explained in the following subsections.

A. Regular RMSA Geometry

The basic rectangular patch antenna geometry is shown in Fig. 1. The antenna was designed to operate at 5.9 GHz and its optimized dimensions (*L*=15.5mm, *W*=16.4mm, h=1.56mm, d=0.5mm, t=1.2mm, s=3.7mm, and tan(δ)=0.0013, RO3003 Rogers Substrate with ε_r =3.0) [22]. The configuration is a microstrip antenna geometry in which the patch and the feed-strip are suspended above the

substrate of height "h" mm. A long-pin probe connector is used to connect the feed strip, which capacitively couples the energy to the main patch. The thorough design procedure of the basic antenna (Fig. 1), parametric studies for the optimization of this geometry, and optimized parameters can be found in earlier works reported by this author [18-22].

To calculate the input impedance of the antenna, the whole geometry is divided into different sections, viz. radiator patch, feed section, and separation between the main patch and a feed strip. These sections can be analyzed individually from well-known circuit theory as explained in [22]. The complete circuit after combining all individual parts is shown in Fig. 2.



(b) Cross sectional view

Fig. 1 Geometry of a rectangular patch antenna with a capacitively coupled feed

In (1), Z_{patch} is the input impedance of the equivalent circuit of the patch, and $Z_{\text{feed_strip}}$ is the input impedance of the feed strip.

The geometry of the antenna shown in Fig. 1 with optimum parameters (L=15.5mm, W=16.4mm, h=1.56mm, d=0.5mm, t=1.2mm, s=3.7mm, and tan(δ)=0.0013, RO3003 Rogers Substrate with ε_r =3.0) was fabricated, and its input return loss characteristics, gain, and radiation patterns were measured. The input impedance model given by (1) was developed in MATLAB. The S₁₁ characteristics obtained from simulation, measurement, and presented model are compared in Fig. 3. It may be noted from Fig. 3 that the developed model values fairly agree with simulated and measured data.



Fig. 2 S₁₁ characteristics comparisons of antenna geometry shown in Fig. 1



Fig. 3 S₁₁ characteristics comparisons of antenna geometry shown in Fig. 1

B. Modified RMSA Geometry

Further to test the design flexibility, the basic model presented in Section II-A has been investigated on the rectangular capacitive coupled modified suspended geometry, as is shown in Fig. 4. The geometry presented here is analyzed as follows. Total geometry is divided into two half parts. The first half is simple rectangular geometry with reduced size by an area covered by staircase shape $(W \ge (L-2a))$ sq. mm. And, the second half is the modified area with two rectangles. The first rectangle with an area of w x 3a sq. mm, and the second rectangle with an area of an x b sq. mm. Besides this, the discontinuities of the microstrip portions are analyzed using the well-known equivalent circuits shown in Fig. 5 (a) & (b) [24]. Based on this, the modeling of this geometry is presented in the following subsection.



(a) Modified RMSA (b) Fabricated prototype Fig. 4 Modified RMSA with capacitive feed





Equations (10) and (11) can be used to calculate the L_1 and L_2 [24]:

$$L_{1} = \frac{L_{w1}}{L_{w1} + L_{w2}} L_{s}$$
⁽²⁾

$$L_2 = \frac{L_{w2}}{L_{w1} + L_{w2}} L_s \tag{3}$$

Where L_{w1} and L_{w2} can be calculated from (4) and (5)

$$L_{w1} = \frac{Z_{0(w1)}\sqrt{\varepsilon_{re(w1)}}}{c} \quad H/m \text{ and}$$
(4)

$$L_{w^{2}} = \frac{Z_{0(w^{2})}\sqrt{\varepsilon_{re(w^{2})}}}{C} \quad H/m$$
 (5)

And, L_s is calculated from the (6) (for <5% error) or from (7) [24]:

$$\frac{L_s}{h}(nH/m) = 40.5 \left(\frac{W_1}{W_2} - 1.0\right) - 75 \log\left(\frac{W_1}{W_2}\right) + 0.2 \left(\frac{W_1}{W_2} - 1\right)^2 \quad (6)$$

Or, by using the more general expression:

$$L_{s} = 0.000987h \left(1 - \frac{Z_{0(w1)}}{Z_{0(w2)}} \sqrt{\frac{\varepsilon_{re(w1)}}{\varepsilon_{re(w2)}}} \right)^{2} (nH)$$
(7)

Like L_s , there are two expressions for evaluating the C_s . It may be noted that (8) gives the values with an error of <10%. Equation (9) can also be used to calculate the values of C_s .

$$\frac{C_s}{\sqrt{W_1 W_2}} (pF/m) = (10.1 \log \varepsilon_r + 2.33) \frac{W_1}{W_2} - 12.6 \log \varepsilon_r - 3.17$$

$$(for \,\varepsilon_r \le 10; 1.5 \le W_1/W_2 \le 3.5)$$
(8)

Or by using (17), which is a more general expression:

$$C_{s} = 0.00137 \frac{\sqrt{\varepsilon_{re(w1)}}}{Z_{0(w1)}} \left(1 - \frac{W_{1}}{W_{2}}\right) h \left[\frac{\varepsilon_{re(w1)} + 0.3}{\varepsilon_{re(w1)} - 0.258}\right] \left[\frac{W_{1}/h + 0.264}{W_{1}/h + 0.8}\right] (pF)$$
(9)

In this paper, expressions (14) and (16) have been used to find the values of L_s and C_s , respectively, as permittivity and transition width ratios (W_1/W_2) match with the specified range of these values.

The geometry shown in Fig. 4 is analyzed by splitting it into the following parts. The first and second transitions indicated by 1 and 2 in Fig. 4 can be represented by three elements, i.e., L_1 , L_2 , and C_{s1} . These elements can be calculated from the equations given in [26]. Further, as explained earlier, the two plate capacitances of two rectangles, $3a \times b$, and $an \times b$, are calculated from the standard parallel plate capacitance equations with some necessary modifications for suspended configurations as given in (10) and (11).

$$C_1 = \frac{\varepsilon_0 \varepsilon_{reff}(3axb)}{(g+h)} \tag{10}$$

$$C_2 = \frac{\varepsilon_0 \varepsilon_{reff}(axb)}{(g+h)} \tag{11}$$

Finally, the *R*, *L*, *C* elements for the reduced patch with an area of $W \ge L-2a$ are calculated. This is the metallic area after subtracting the modified area from the main patch ($W \ge L$). This can be modeled as a patch equivalent, i.e., using parallel R_{patch} , L_{patch} , and C_{patch} elements with total length L is replaced by reduced length (*L*-2*b*). These values may be computed from (12)-(15).

$$C_{patch} = \frac{\varepsilon_0 \varepsilon_{reff} W(L-2b)}{2(g+h)}$$
(12)

$$L_{patch} = \frac{1}{C_{patch}\omega_c^2}$$
(13)

$$R_{patch} = \frac{Q_T}{\omega_c C_{patch}}$$
(14)

In the end, all these subsections are clubbed to get the final model, which is shown in Fig. 6.



Fig. 6 Derived equivalent circuit for the antenna geometry shown in Fig. 4

The total impedance is calculated by using the circuit theory approach from Fig. 6, which is an equivalent circuit of the geometry shown in Fig. 4 as follows. The reduced patch area's input impedance can be written as:

$$Z_{reduced_patch} = Y_{patch}^{-1} = \left[\frac{1}{R_{patch}} + j\left(\omega C_{patch} - \frac{1}{\omega L_{patch}}\right)\right]^{-1}$$
(15)

In (15) C_{patch} , L_{patch} , R_{patch} are as given in (12)-(14). Further, simplifying the circuit shown in Fig. 6 as:

$$Z_A = Z_{reduced_patch} + j\omega L_1$$
(16)

$$Z_{B} = \left[\frac{1}{Z_{A}} + j\omega C_{s1} \right]^{T}$$
(17)

$$Z_C = Z_B + j\omega L_2 \tag{18}$$

$$Z_{D} = \left[\frac{1}{Z_{C}} + j\omega C_{1}\right]^{-1}$$
(19)

$$Z_E = Z_D + j\omega L_3 \tag{20}$$

$$Z_F = \left[\frac{1}{Z_E} + j\omega C_{s2} \right]^{-1}$$
(21)

$$Z_G = Z_F + j\omega L_4 \tag{22}$$

$$Z_{patch} = \left[\frac{1}{Z_G} + j\omega C_2\right]^{-1}$$
(23)

Where impedance expressions from Z_A to Z_G are cumulative values, feed, section, and coupling between feed-strip and main patch expressions remain the same as that of the basic patch model and all other geometries presented so far. Finally, the total input impedance is calculated using (1). The input characteristics obtained for this geometry are presented in Fig. 7. From Fig. 7 it may be noted that the modeled S₁₁ curve closely follows the simulated curve except S₁₁ depth mismatch at first resonance. However, the depth of S₁₁ does not make sense when it is already below -10dB.



Fig. 7 S₁₁ characteristics comparison of modified RMSA antenna geometry

III. INPUT IMPEDANCE OF OTHER GEOMETRIES

The model obtained in Section II has been further investigated for triangular and semi-ellipse geometries. Triangular and semi-ellipse geometries equivalent circuit elements are calculated by equating the area of these geometries to circular microstrip and are discussed in the following subsections. Once the equivalent elements of these geometries are computed, these values can be used to find the input impedance using the main equation (1). Analysis of all these geometries has been presented in the following subsections.

A. Equilateral Triangular Microstrip Antenna (ETMSA)

The antenna geometry used for simulation and analysis is shown in Fig. 8. The triangular patch was designed to operate for the same resonant frequency (5.9GHz) and has the dimensions of base=19.75mm and height=17.1mm, and then it was optimized for maximum impedance bandwidth using the key design parameters (separation between the main antenna element and feed strip (d) = 0.5mm, and feed strip dimensions t=2.0mm and s=4.5mm). The feed is located parallel to the base, as shown in Fig. 8. The total capacitance (parallel plate and fringing) of ETMSA can be found by equating its area with the rectangular/circular microstrip geometry.



(a) ETMSA geometry (b) Prototype Fig. 8 Geometry of an equilateral triangular microstrip antenna (ETMSA)

Equation (24) represents the ETMSA's total capacitance which is obtained from the circular microstrip disc capacitance by replacing 'a' with ' a_{eq} ' [25].

$$C_{T} = \frac{a_{eq} \cdot \sigma_{r} \cdot \sigma_{0}}{h} \left[1 + \frac{2n}{a_{eq} \pi \varepsilon_{r}} \left\{ \ln \left(\frac{a_{eq}}{2h} \right) + (1.41\varepsilon_{r} + 1.77) + \frac{n}{a_{eq}} (0.268\varepsilon_{r} + 1.65) \right\} \right]$$
(24)

In Equation (24), ' a_{eq} ' is given by

$$a_{eq} = \sqrt{\frac{A_{triangle}}{\pi}}$$
(25)

In (25), ' $A_{triangle}$ ' represents the area of the original triangle.

From (26), the parallel plate capacitance and the fringing capacitances can be written after necessary air-dielectric medium corrections as:

$$C_{patch} = \frac{a_{eq}^{2} \pi \mathcal{E}_{reff} \mathcal{E}_{0}}{\left(g+h\right)}$$

$$C_{p1} = C_{patch} \left[\frac{2(g+h)}{a_{eq} \pi \mathcal{E}_{reff}} \left\{ \ln\left(\frac{a_{eq}}{2(g+h)}\right) + \left(1.41\mathcal{E}_{reff} + 1.77\right) + \frac{(g+h)}{a_{eq}} \left(0.268\mathcal{E}_{reff} + 1.65\right) \right\}$$

$$(27)$$

Equation (26) can be used in place of rectangular patch capacitance (C_{patch} shown in Fig. 2), and (27) can be used to calculate one of the parallel capacitors (C_{pl}). Other elements of Fig. 2 can be calculated as explained in [19-22]. The S₁₁ characteristics obtained for the triangular microstrip patch antenna are shown in Fig. 9. From Fig. 9, it can be noticed that there is a proper matching between the simulated and calculated results in the band of operation (-10dB bandwidth range).

B. Semi-elliptical Microstrip Patch Antenna

The geometry of semi-elliptical microstrip patch antenna geometry is shown in Fig. 10. The semi-elliptical microstrip antenna was designed for the best possible bandwidth (4.1GHz to 6.6GHz = 2.5GHz) as suggested in [26] with the dimensions indicated in Fig. 10 (a=19mm, b=8.23mm, d=0.5mm, s=4.5mm and t=1.6mm). The total capacitance of the semi-ellipse can be calculated by considering the complete elliptical geometry and then taking half the total capacitance to represent the semi-ellipse. An aperture dimension of a semi-elliptical patch is calculated by considering an equivalent circular patch having the same "metal area". Therefore the semi-elliptical area is calculated by considering two circles of radii 'a' and 'b' as suggested in [26] for the elliptical patch. It should be noted that in Fig. 10 'a' and 'b' are the semi-major and semi-minor axes of ellipse geometry.



Fig. 9 S₁₁ characteristics comparison of ETMSA



(a) Elipse geometry (b) Prototype Fig. 10 Geometry of semi-elliptical microstrip antenna

$$A_{semi-ellipse} = 0.5\sqrt{A_{circle1} \times A_{circle2}} = \frac{\pi}{2}ab$$
(28)

With the fringing fields included, it can be written as

$$A_{semi-ellipse} = \frac{\pi}{2} a_{eq} b_{eq} \,. \tag{29}$$

Where

$$a_{eq} = \left[a^2 + \frac{2ah}{\pi\varepsilon_{reff}} \left\{ \ln\left(\frac{a}{2h}\right) + (1.41\varepsilon_{reff} + 1.77) + \frac{h}{a}(0.268\varepsilon_{reff} + 1.65) \right\} \right]^{\frac{1}{2}}$$
(30)

Similarly, b_{eq} can be calculated by replacing the term 'a' in (30) with 'b'. Therefore, the total capacitance for the elliptical patch is written as:

$$C_T = \frac{\varepsilon_0 \varepsilon_r \pi a_{eq} b_{eq}}{2(g+h)} \tag{31}$$

Similar to the previous case (triangular patch), (31) can be used to write the patch and fringing capacitances separately,

and also complex air-dielectric corrections have to be made before calculation. Like the above two cases (rectangular and triangular patch geometries), here also the calculated and simulated S_{11} characteristics are closely matching and are depicted in Fig. 11.



Fig. 11 S₁₁ characteristics comparison for semi-ellipse geometry

IV. CONCLUSIONS

The model developed based on the equivalent circuit theory approach to find the input impedance has been presented. The models proposed here are suitable for evaluating the antenna's input impedance, including the effects of the feed strip and the probe pin, which are located outside the main radiating element, over an operating bandwidth. Return loss values evaluated using the proposed models fairly match with the simulated and experimental results. Further, the concept has been extended to a modified rectangular microstrip antenna (one side staircase shaped rectangular patch). Here the model equations are derived from the equivalent circuits available for microstrip discontinuities with necessary modifications for suspended geometries. Further, the model developed for rectangular microstrip patch antenna can be extended to all regular microstrip geometries by equating their area with RMSA. Like RMSA, the calculated input return loss characteristics (S₁₁) Triangular and Semi-ellipse were also found to be in good agreement with the simulated results. For these two geometries, also good results have been obtained like other cases demonstrated.

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